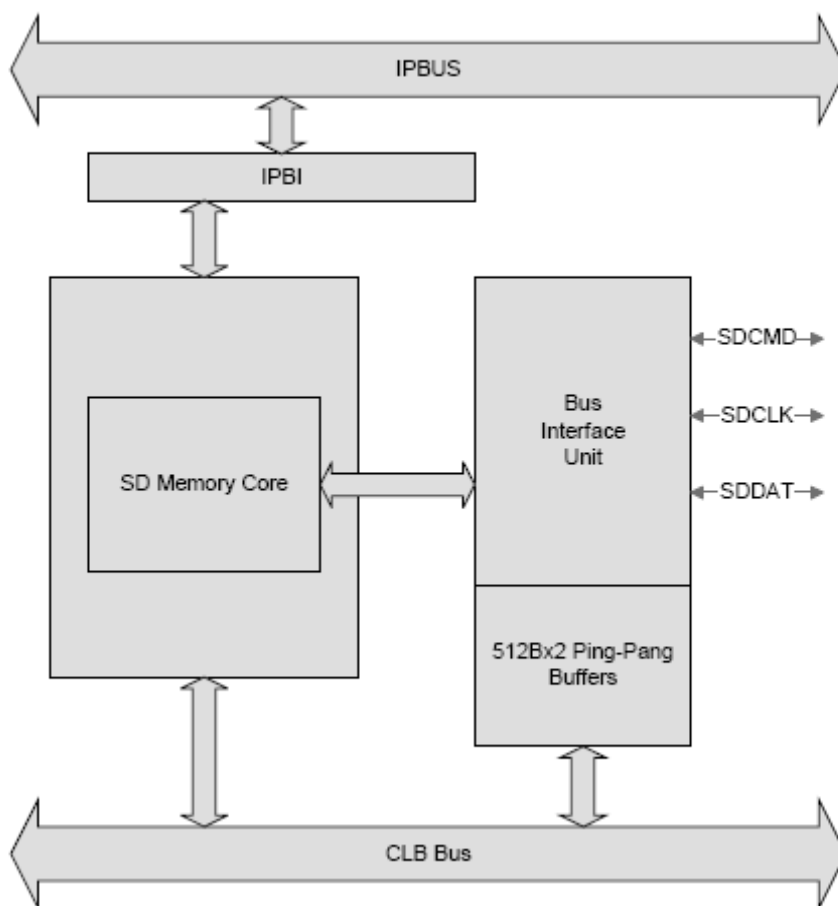


SD2.0 Controller

Introduction

The SD Device Controller (SDDC) is designed to support SD Memory card specification (Version 2.00)



Block Diagram of SDDC

SDDC Features

- Compatible with the SD Memory Card Specification 2.00
- SD Memory Card Security Protocol support(Command only)
- Support data communication rate of 0-50MHz.
- 512Bytes Single-Port Ping-Pong Buffers.
- 1-bit SD mode ,4-bit SD mode and SPI mode support.
- Support Card Lock/Unlock Operation.

- Support Write Protect Operation. frequency with any SD Clock
- The chip can run at any system clock

Performance and Characteristics

- Frequency (WCS): 60 MHz
- Cell Size: 0.3 mm²
- Process: 0.18μm (WCS, 1.62V, 120°)

Application Examples

- SD Card

Availability

- Q2, 2009

*To obtain more information about the SD2.0 or other C*Core™ products, please contact the C*Core Technology Co., Ltd. by phone: 0512-68091375, email: support@china-core.com or web: <http://www.china-core.com>.*

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