

SATA Core Data Sheet

Summary

The SATA Device Core implements the Serial Advanced Technology Attachment (SATA) storage interface for physical storage devices.

AES Engine Features

- Supports SATA 1.5 Gb/s, SATA 3.0 Gb/s, and SATA 6.0 Gb/s speeds
- Compliant with Serial ATA Specification 2.6
- Supports Command Layer firmware
- Highly configurable PHY interface
- Provides additional user defined PHY status and control ports
- Optional Rx Data Buffer for recovered clock systems
- Optional data alignment circuitry when Rx Data Buffer is also included
- Optional OOB signalling detection and generation
- SATA 1.5 Gb/s, SATA 3.0 Gb/s, and SATA 6.0 Gb/s speed negotiation
- when Tx OOB signaling is Selected
- Optional 8b/10b encoding/decoding
- Supports power management features including automatic Partial to Slumber transition
- Supports SATA BIST loopback modes
- Configurable AMBA AHB interface (one master and one slave)
- Internal high-performance DMA engine with flexible programming model
- Supports disabling Rx and Tx Data clocks during power down modes

Availability

- Q1, 2011

To obtain more information about the DSATA or other C*Core™ products, please contact the C*Core Technology Co., Ltd. by phone: 0512-68091375, email: support@china-core.com or web: <http://www.china-core.com>.

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